



S/N 09/964,010

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Matthew B. Haycock et al.	Examiner:	Tse W. Chen
Serial No.:	09/964,010	Group Art Unit:	2116
Filed:	September 26, 2001	Docket No.:	884.455US1
Title:	METHOD AND APPARATUS FOR REALIGNING BITS ON A PARALLEL BUS		
Assignee:	Intel Corporation	Customer Number:	21186

SUPPLEMENTAL AMENDMENT & RESPONSE UNDER § 1.116

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Commissioner for Patents
P.O. Box 1450
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In response to the Advisory Action mailed December 13, 2005, please amend the application as follows.

This response is accompanied by a Petition, as well as the appropriate fee, to obtain an additional two-month extension of the period for responding to the Office Action, thereby moving the deadline for response from October 29, 2005 to January 29, 2006 (Sunday). Since January 29, 2005 is Sunday, the deadline with the extension is Monday, January 30, 2006. Applicant submitted a petition for a one-month extension of time along with the appropriate fee on November 29, 2005.